

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

KIMURA et al

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Appl. No. 10/670,194

TC/A.U. 2811

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Examiner: Im, Junghwa M.

For: CHIP-STACK SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD OF
THE SAME

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July 2, 2008

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

RESPONSE

Responsive to the Official Action dated April 3, 2008, Applicants provide the following
remarks.